

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

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PATENT NO.: 6,990,650 B2
DATED : January 24, 2006
INVENTOR(S) : Steven Teig et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Claim 3,

Column 43, Lines 64-65, "Boolean functions includes two or more output boolean functions."

should read -- Boolean functions includes two or more output Boolean functions. --

Claim 5,

Column 44, Line 4, "after terminating the repetition, traversing the design to"

should read -- after terminating the repetitions, traversing the design to --

Lines 9-14,

"structure;

if at least one replacement sub-network for an identified circuit element is identified, the circuit element in the design with the identified replacement sub-network. functions includes two or more output Boolean functions."

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PATENT NO. 6,990,650 B2

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.
PTO/SB/44 (07-03)

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PATENT NO.: 6,883,154 B2
DATED: April 19, 2005
INVENTOR(S): Steven Teig et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

should read --structure; and
if at least one replacement sub-network for an identified circuit element is identified, replacing the circuit element in the design with the identified replacement sub-network.--

Claim 6.

Column 44, Line 15, "The method of claim 5 wherein if more than one"

should read -- The method of claim 5, wherein if more than one --

Claim 10.

Column 44, Lines 42-43, "replacing the selected candidate sub-networki in the design with the replacement sub-network."

should read --replacing the selected candidate sub-network in the design with the replacement sub-network.--

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DATED: January 24, 2006
INVENTOR(S): Steven Teig et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Claim 13.

Column 45, Lines 6-7 reads ... "the replacement sub-network;"

should read -- the replacement sub-network; and --

Claim 15.

Column 46, Line 6 reads "a seventh set of instructions for attempting to identify, for"

should read -- a seventh set of instructions for attempting to identify, for --

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